



Shri Vile Parle Kelavani Mandal's

DWARKADAS J. SANGHVI COLLEGE OF ENGINEERING

(Autonomous College Affiliated to the University of Mumbai)

NAAC Accredited with "A" Grade (CGPA : 3.18)



REGULAR EXAMINATION TIMETABLE, MAY, 2026
MASTER OF TECHNOLOGY (M. Tech.) All Branches
FIRST YEAR, SEMESTER – II (For 2025-26 Batch Under Autonomy)
Timings: 02.30 p.m. to 04.30 p.m.

Date: 9th April, 2026

Day & Date	Electronics & Telecommunication Engineering	Computer Engineering
Monday, 18/05/2026	Advanced Wireless Communication Networks	Big Data Infrastructure
Wednesday, 20/05/2026	Remote Sensing Concepts	Computational Intelligence
Friday, 22/05/2026	RF and Microwave Engineering	Computational Linguistics
Monday, 25/05/2026	Project Management	

Important Note: Changes if any, in the timetable shall be communicated on the College website (djsce.ac.in).

Ms. Alka Shukla
Controller of Examinations

Dr. Hari Vasudevan
Principal